

FIG. 1 is a schematic diagram of a system for

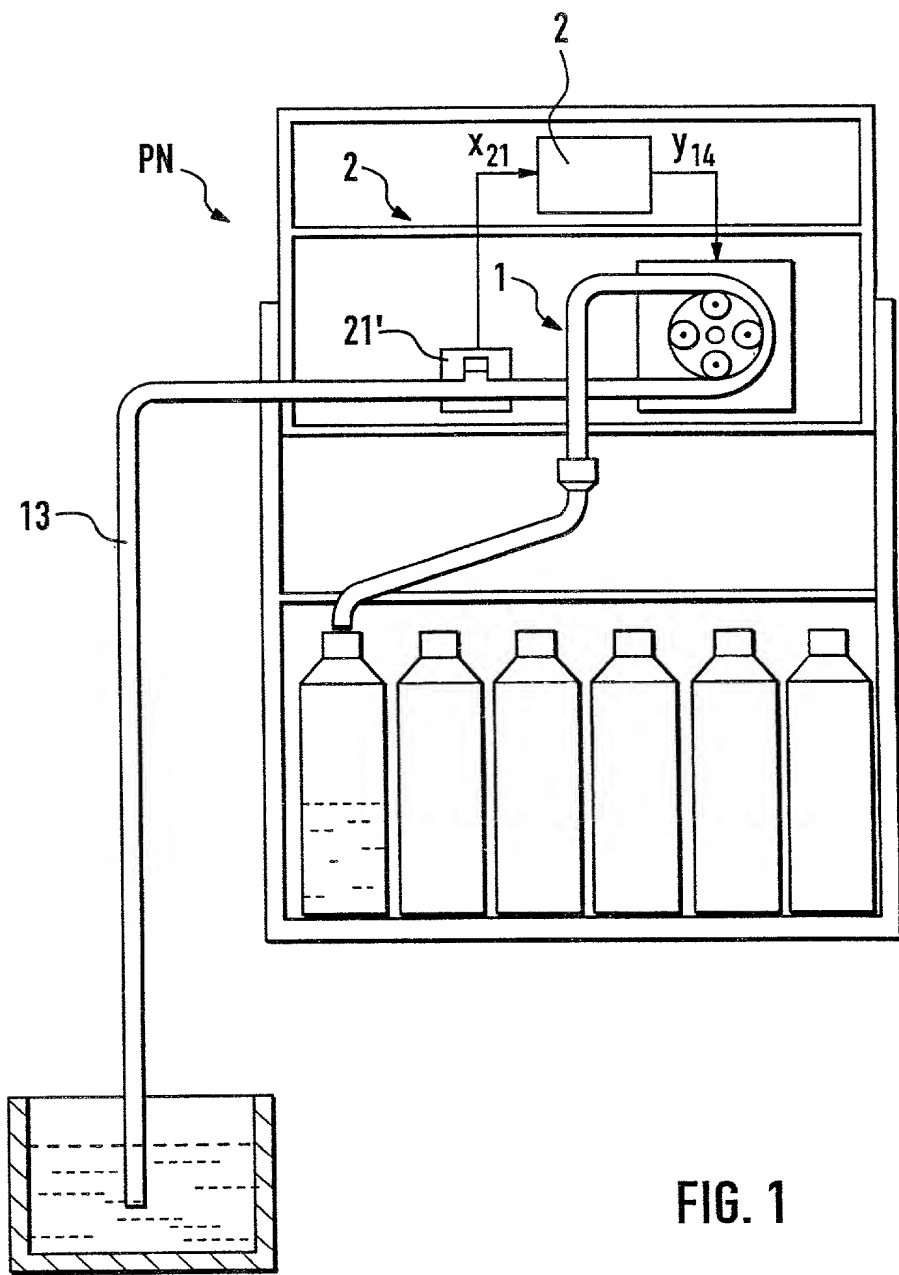
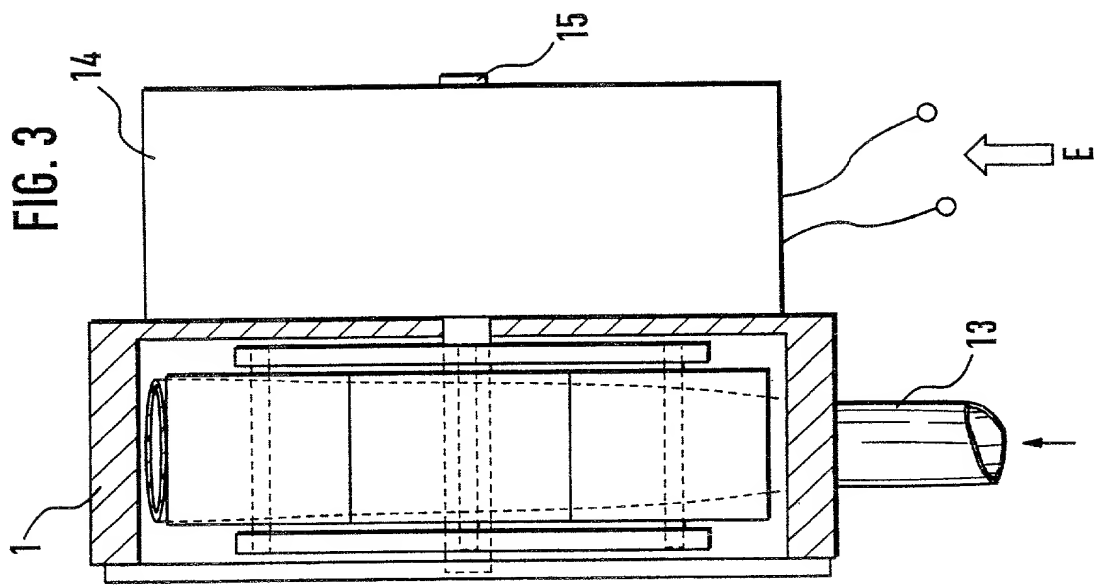
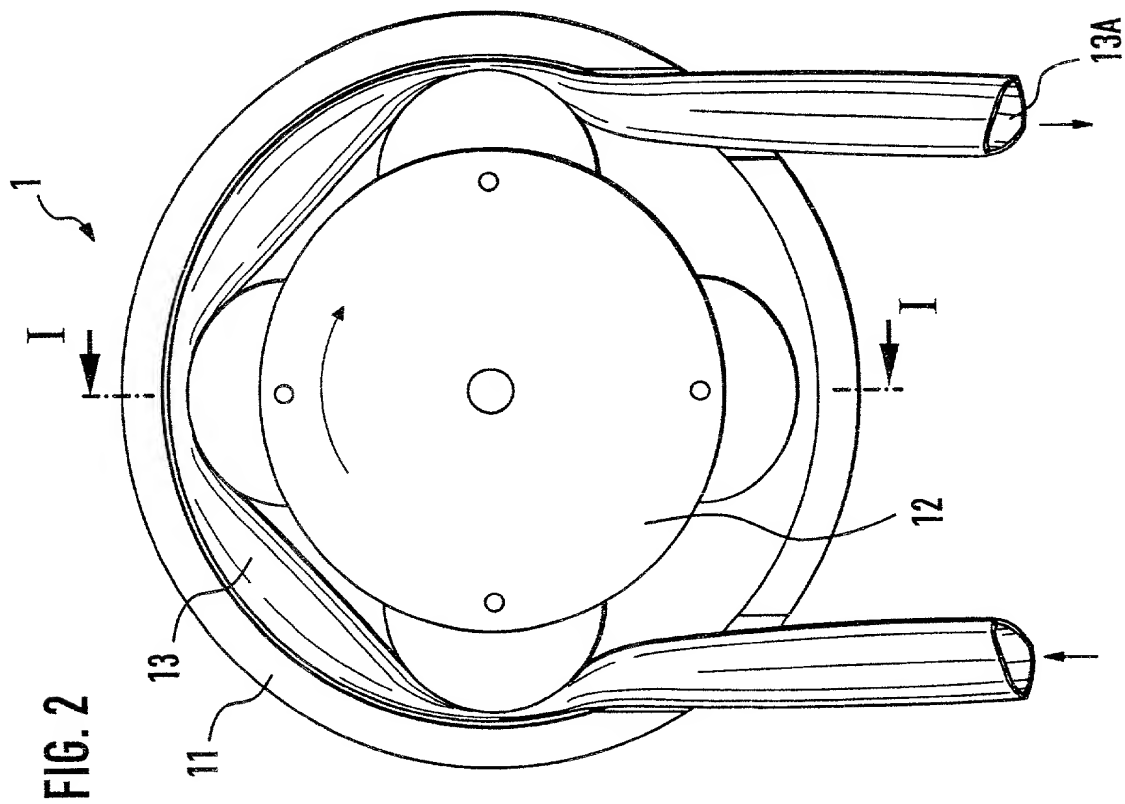


FIG. 1



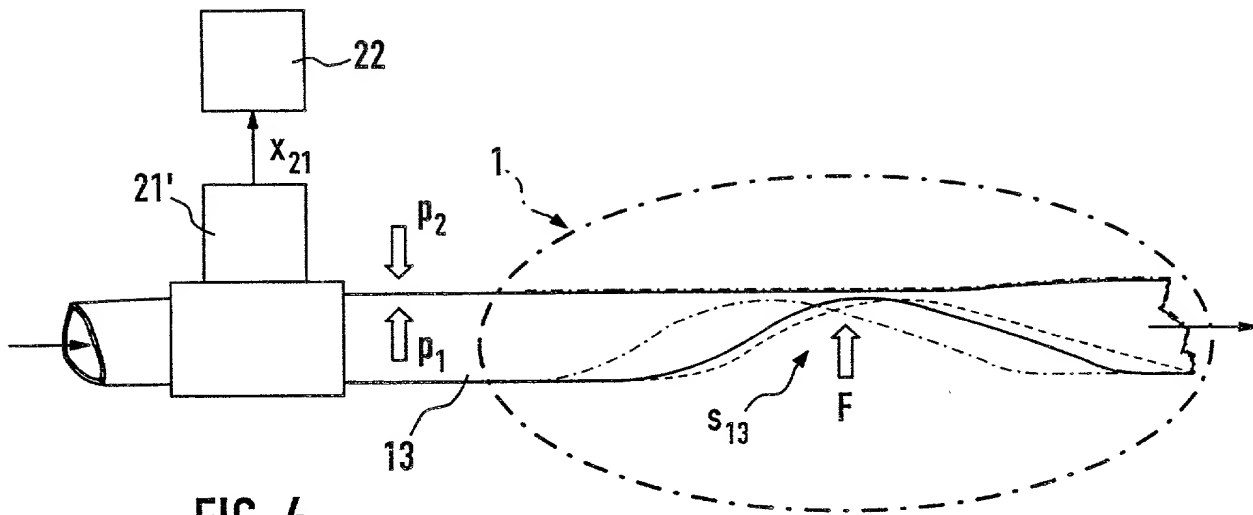


FIG. 4

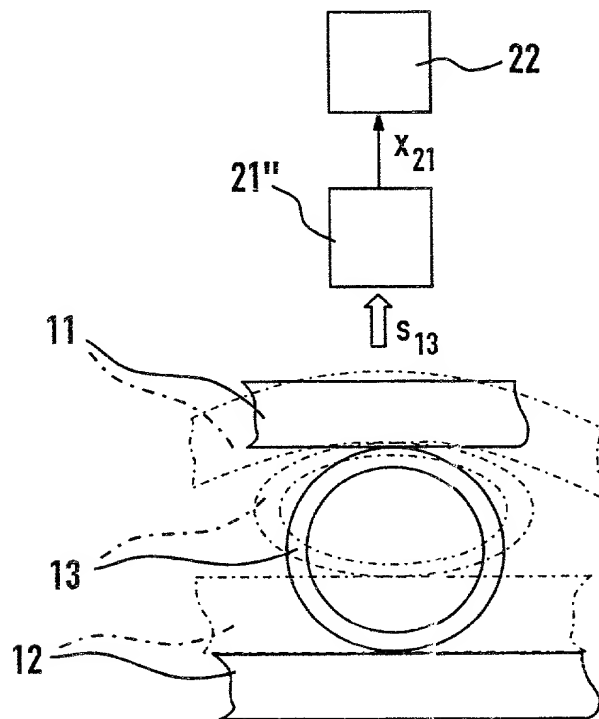


FIG. 5

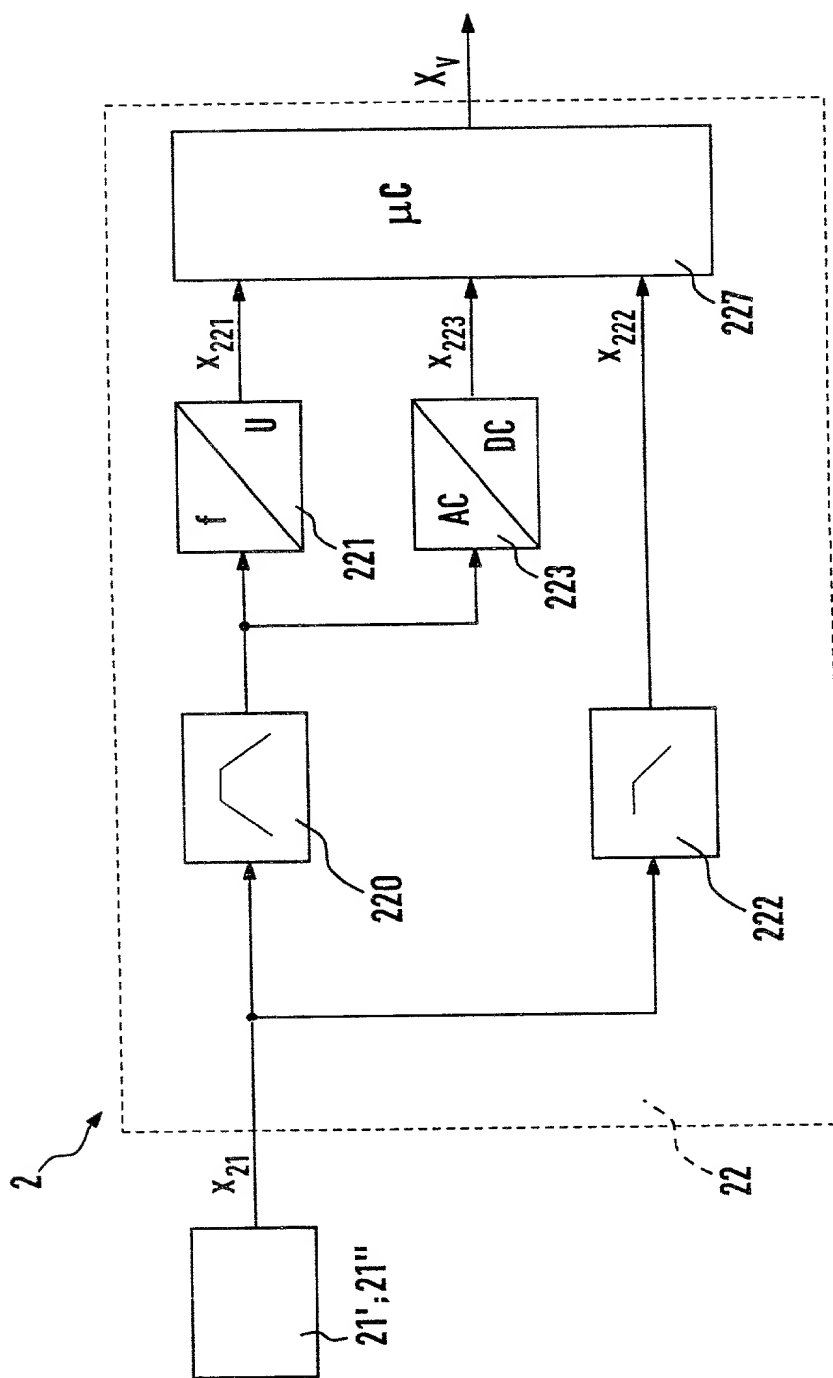


FIG. 6

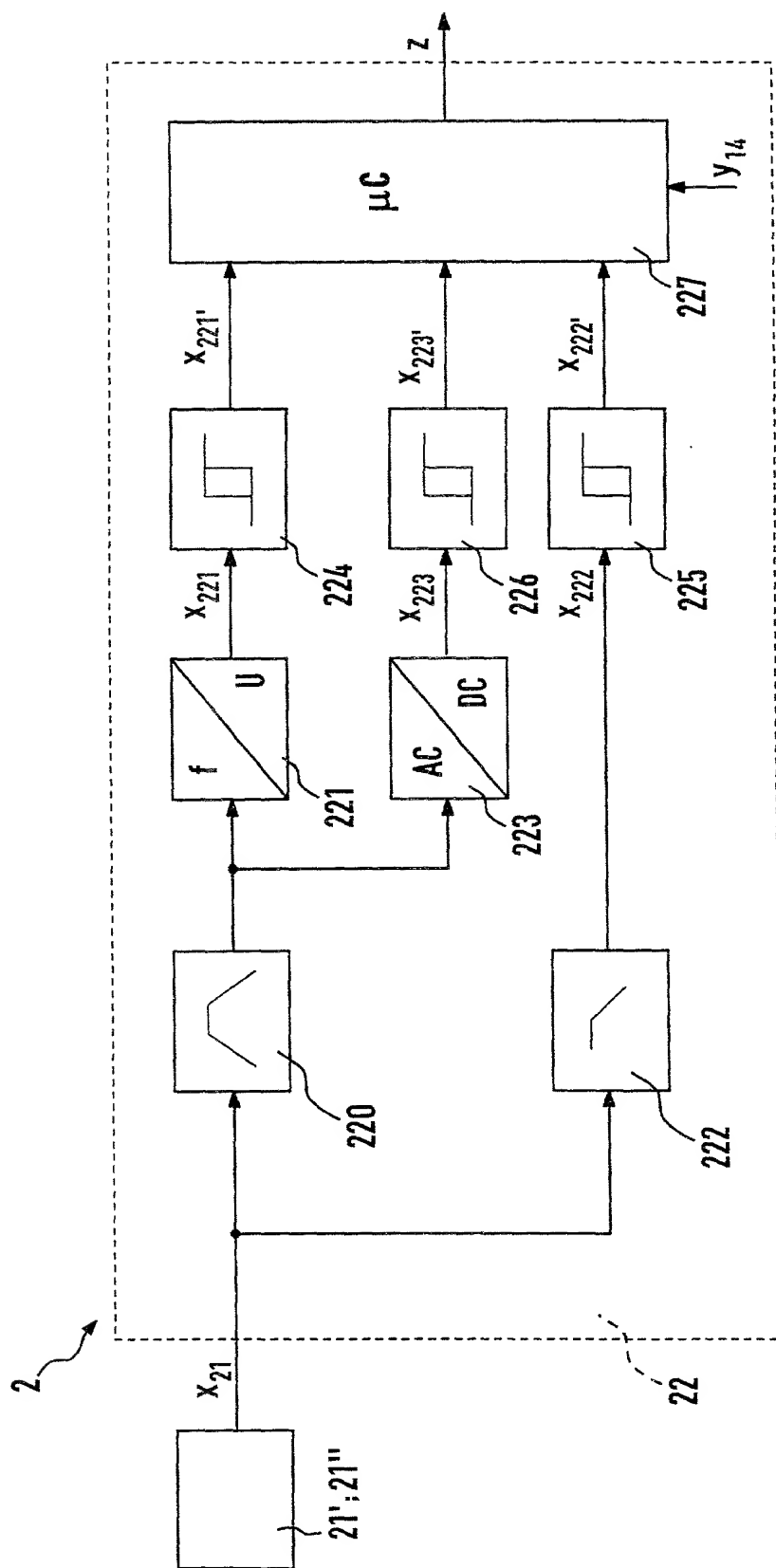


FIG. 7

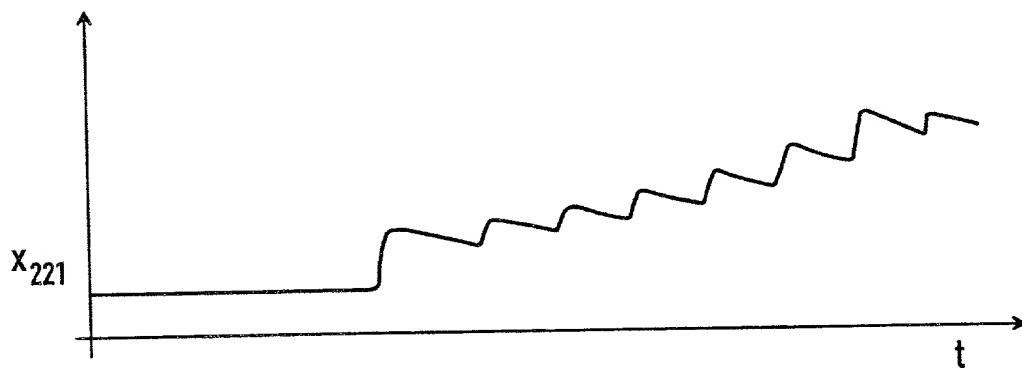
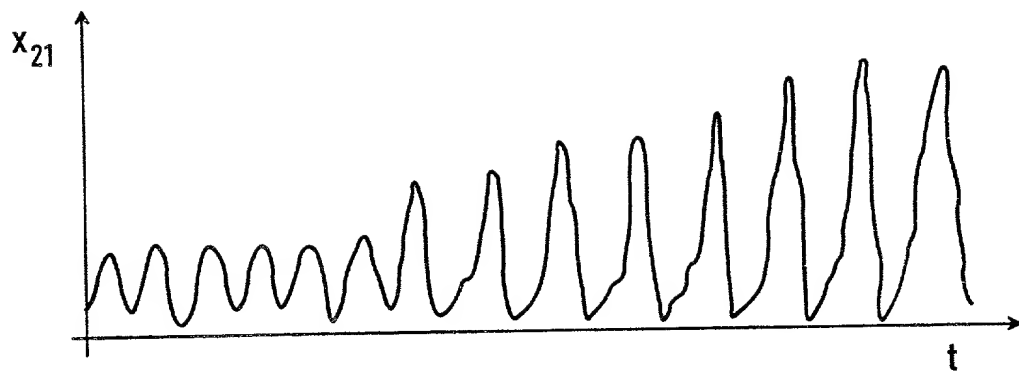


FIG. 8